

Day : Tuesday
Date: 6/21/2005

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Inventor Information for 09/597190

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KRAUSE, JOHN	GEORGETOWN	TEXAS
WHITESIDE, PATRICIA L.	AUSTIN	TEXAS

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Inventor Name Search Result

Your Search was:

Last Name = BUNTON

First Name = WILLIAM

Application#	Patent#	Status	Date Filed	Title	Inventor Name 44
<u>60227899</u>	Not Issued	159	08/25/2000	CLUSTERED COMPUTER SYSTEM AND A METHOD FOR FORMING AND CONTROLLING THE CLUSTERED COMPUTER SYSTEM	BUNTON, WILLIAM
<u>60050819</u>	Not Issued	159	06/25/1997	ROUTER-BASED NETWORK WITH POINT-TO-POINT LINK CONNECTORS HAVING SCALABLE BANDWIDTH	BUNTON, WILLIAM PATTERSON
<u>60009222</u>	Not Issued	159	12/26/1995	DEADLOCK AVOIDANCE AND MEMORY SPILL APPARATUS	BUNTON, WILLIAM P.
<u>10823182</u>	<u>6882656</u>	150	04/13/2004	SPECULATIVE TRANSMIT FOR SYSTEM AREA NETWORK LATENCY REDUCTION	BUNTON, WILLIAM P.
<u>10680913</u>	Not Issued	030	10/08/2003	HIGH-SPEED INTERCONNECTION ADAPTER HAVING AUTOMATED LANE DE-SKEW	BUNTON, WILLIAM P.
<u>10061435</u>	Not Issued	094	01/31/2002	SYSTEM RECOVERY FROM ERRORS FOR PROCESSOR AND ASSOCIATED COMPONENTS	BUNTON, WILLIAM P.
<u>09989911</u>	Not Issued	030	11/19/2001	TIME-DIVISION MULTIPLEXED LINK FOR USE IN A SERVICE AREA NETWORK	BUNTON, WILLIAM P.
<u>09989897</u>	Not Issued	061	11/19/2001	TIME-DIVISION AND WAVE-DIVISION MULTIPLEXED LINK FOR USE IN A SERVICE AREA NETWORK	BUNTON, WILLIAM P.

<u>09974747</u>	<u>6820135</u>	150	10/10/2001	MODELESS EVENT-DRIVEN DATA TRANSFORMATION	BUNTON, WILLIAM G.
<u>09935440</u>	Not Issued	071	08/22/2001	CLUSTERED COMPUTER SYSTEM AND A METHOD OF FORMING AND CONTROLLING THE CLUSTERED COMPUTER SYSTEM	BUNTON, WILLIAM
<u>09671940</u>	<u>6738344</u>	150	09/27/2000	LINK EXTENDERS WITH LINK ALIVE PROPAGATION	BUNTON, WILLIAM P.
<u>09671938</u>	<u>6765922</u>	150	09/27/2000	SPECULATIVE TRANSMIT FOR SYSTEM AREA NETWORK LATENCY REDUCTION	BUNTON, WILLIAM P.
<u>09670854</u>	<u>6728909</u>	150	09/26/2000	DATA COMMUNICATION WITH SPECULATIVE RECEPTION OF DATA IN A DATA PROCESSING SYSTEM	BUNTON, WILLIAM P.
<u>09670174</u>	Not Issued	092	09/25/2000	SYSTEM AND METHOD FOR CONFIGURING ADAPTIVE SETS OF LINKS BETWEEN ROUTERS IN A SYSTEM AREA NETWORK (SAN)	BUNTON, WILLIAM P.
<u>09661214</u>	Not Issued	077	09/14/2000	METHOD AND APPARATUS FOR TRANSFERRING DATA	BUNTON, WILLIAM P.
<u>09652473</u>	<u>6795868</u>	150	08/31/2000	SYSTEM AND METHOD FOR EVENT-DRIVEN DATA TRANSFORMATION	BUNTON, WILLIAM G.
<u>09606425</u>	Not Issued	041	06/28/2000	NETWORK DEADLOCK AVOIDANCE USING OTHER MEMORY TO BUFFER INCOMING MESSAGE PACKETS	BUNTON, WILLIAM P.
<u>09597192</u>	<u>6865231</u>	150	06/20/2000	HIGH-SPEED INTERCONNECTION ADAPTER HAVING AUTOMATED CROSSED DIFFERENTIAL PAIR CORRECTION	BUNTON, WILLIAM P.
<u>09597190</u>	Not Issued	080	06/20/2000	HIGH-SPEED INTERCONNECTION LINK HAVING AUTOMATED LANE REORDERING	BUNTON, WILLIAM P.
<u>09596980</u>	<u>6690757</u>	150	06/20/2000	HIGH-SPEED INTERCONNECTION	BUNTON, WILLIAM P.

				ADAPTER HAVING AUTOMATED LANE DE- SKEW	
<u>09415731</u>	<u>6870814</u>	150	10/12/1999	LINK EXTENDERS WITH ERROR PROPAGATION AND REPORTING	BUNTON, WILLIAM P.
<u>09231407</u>	<u>6360284</u>	150	01/13/1999	SYSTEM FOR PROTECTING OUTPUT DRIVERS CONNECTED TO A POWERED-OFF LOAD	BUNTON, WILLIAM P.
<u>09206593</u>	<u>6175882</u>	150	12/07/1998	NETWORK SYSTEM FOR A FIRST MODULE PORT AUTO CONFIGURING SAME MODE AS A SECOND MODULE PORT	BUNTON, WILLIAM P.
<u>09206481</u>	<u>6249756</u>	150	12/07/1998	HYBRID FLOW CONTROL	BUNTON, WILLIAM P.
<u>09143272</u>	<u>6374282</u>	150	08/28/1998	METHOD AND APPARATUS FOR TRACKING MULTI- THREADED SYSTEM AREA NETWORK (SAN) TRAFFIC	BUNTON, WILLIAM P.
<u>09001100</u>	<u>6157967</u>	150	12/30/1997	METHOD OF COMMUNICATING DATA ELEMENTS BETWEEN UNITS OF A PROCESSING SYSTEM USING BUSY/READY COMMANDS	BUNTON, WILLIAM PATTERSON
<u>08762653</u>	<u>6151689</u>	150	12/09/1996	FAIL-FAST, FAIL-TOLERANT MULTIPROCESSOR SYSTEM	BUNTON, WILLIAM PATTERSON
<u>08579376</u>	<u>5861684</u>	150	12/27/1995	FLEXIBLE IMPLEMENTATION OF DISTRIBUTED DC POWER	BUNTON, WILLIAM P.
<u>08578883</u>	<u>5548463</u>	150	01/11/1996	SYSTEM FOR SWITCHING POWER AND SCRUBBING POWER MIXING DEVICES FOR FAULTS	BUNTON, WILLIAM P.
<u>08485217</u>	<u>5751932</u>	150	06/07/1995	FAIL-FAST, FAIL- FUNCTIONAL, FAULT- TOLERANT MULTIPROCESSOR SYSTEM	BUNTON, WILLIAM P.
<u>08485055</u>	<u>6233702</u>	150	06/07/1995	SELF-CHECKED, LOCK-STEP PROCESSOR PAIRS	BUNTON, WILLIAM P.
<u>08485053</u>	<u>5790776</u>	150	06/07/1995	APPARATUS FOR DETECTING DIVERGENCE BETWEEN A PAIR OF	BUNTON, WILLIAM P.

				DUPLEXED, SYNCHRONIZED PROCESSOR ELEMENTS	
<u>08482628</u>	<u>5574849</u>	150	06/07/1995	SYNCHRONIZED DATA TRANSMISSION BETWEEN ELEMENTS OF A PROCESSING SYSTEM	BUNTON, WILLIAM P.
<u>08482618</u>	<u>5964835</u>	150	06/07/1995	STORAGE ACCESS VALIDATION TO DATA MESSAGES USING PARTIAL STORAGE ADDRESS DATA INDEXED ENTRIES CONTAINING PERMISSIBLE ADDRESS RANGE VALIDATION FOR MESSAGE SOURCE	BUNTON, WILLIAM PATTERSON
<u>08481749</u>	<u>5675807</u>	150	06/07/1995	INTERRUPT MESSAGE DELIVERY IDENTIFIED BY STORAGE LOCATION OF RECEIVED INTERRUPT DATA	BUNTON, WILLIAM P.
<u>08479473</u>	Not Issued	161	06/07/1995	METHOD OF COMMUNICATING DATA ELEMENTS BETWEEN UNITS OF A PROCESSING SYSTEM	BUNTON, WILLIAM P.
<u>08474770</u>	<u>5675579</u>	150	06/07/1995	METHOD FOR VERIFYING RESPONSES TO MESSAGES USING A BARRIER MESSAGE	BUNTON, WILLIAM P.
<u>08473541</u>	<u>5751955</u>	150	06/07/1995	METHOD OF SYNCHRONIZING A PAIR OF CENTRAL PROCESSOR UNITS FOR DUPLEX, LOCK-STEP OPERATION BY COPYING DATA INTO A CORRESPONDING LOCATIONS OF ANOTHER MEMORY	BUNTON, WILLIAM P.
<u>08472222</u>	Not Issued	161	06/07/1995	DATA COMMUNICATION IN A DATA PROCESSING SYSTEM	BUNTON, WILLIAM P.
<u>08269187</u>	<u>5436827</u>	150	06/30/1994	CONTROL INTERFACE FOR CUSTOMER REPLACEABLE FAN UNIT	BUNTON, WILLIAM P.
<u>08268699</u>	Not Issued	166	06/30/1994	A SYSTEM FOR SWITCHING POWER AND SCRUBBING POWER MIXING DEVICES FOR FAULTS	BUNTON, WILLIAM P.

<u>07996728</u>	<u>5293636</u>	150	12/24/1992	MODULE FOR INSERTION INTO A MULTI-MODULE SYSTEM HAVING ELECTRONIC KEYING FOR PREVENTING POWER TO IMPROPERLY CONNECTED MODULES WITH IMPROPERLY CONFIGURED DIODE CONNECTIONS	BUNTON, WILLIAM P.
<u>07992944</u>	Not Issued	161	12/17/1992	FAIL-FAST, FAIL-FUNCTIONAL, FAULT-TOLERANT MULTIPROCESSOR SYSTEM	BUNTON, WILLIAM P.
<u>07464817</u>	<u>5203004</u>	150	01/08/1990	MULTI-BOARD SYSTEM HAVING ELECTRONIC KEYING AND PREVENTING POWER TO IMPROPERLY CONNECTED PLUG-IN BOARDS WITH IMPROPERLY CONFIGURED DIODE CONNECTIONS	BUNTON, WILLIAM P.

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Patent Assignment Abstract of Title

Total Assignments: 4

Application #: 09597190 **Filing Dt:** 06/20/2000 **Patent #:** NONE **Issue Dt:**
PCT #: NONE **Publication #:** NONE **Pub Dt:**

Inventors: William P. Bunton, John Krause, Patricia L. Whiteside

Title: High-speed interconnection link having automated lane reordering

Assignment: 1

Reel/Frame: <u>010886/0634</u>	Received: 07/06/2000	Recorded: 06/20/2000	Mailed: 08/22/2000	Pages: 4
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Conveyance: ASSIGNMENT OF ASSIGNORS INTEREST (SEE DOCUMENT FOR DETAILS).

Assignors: BUNTON, WILLIAM P. **Exec Dt:** 04/19/2000
WHITESIDE, PATRICIA L. **Exec Dt:** 06/19/2000

Assignee: COMPAQ COMPUTER CORPORATION
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Correspondent: CONLEY, ROSE & TAYON, P.C.
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Assignment: 2

Reel/Frame: <u>011287/0083</u>	Received: 12/05/2000	Recorded: 11/13/2000	Mailed: 02/05/2001	Pages: 4
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Conveyance: ASSIGNMENT OF ASSIGNORS INTEREST (SEE DOCUMENT FOR DETAILS).

Assignor: KRAUSE, JOHN **Exec Dt:** 10/16/2000

Assignee: COMPAQ COMPUTER CORPORATION
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Assignment: 3

Reel/Frame: <u>012476/0520</u>	Received: 01/23/2002	Recorded: 01/15/2002	Mailed: 03/14/2002	Pages: 7
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Conveyance: ASSIGNMENT OF ASSIGNORS INTEREST (SEE DOCUMENT FOR DETAILS).

Assignor: COMPAQ COMPUTER CORPORATION **Exec Dt:** 06/20/2001

Assignee: COMPAQ INFORMATION TECHNOLOGIES GROUP, L.P.
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Assignment: 4

Reel/Frame: <u>014177/0428</u>	Received: 12/02/2003	Recorded: 12/02/2003	Mailed: 12/08/2003	Pages: 11
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L2	7	1 and (infiniband\$4 or ngio)	US-PGPUB; USPAT; EPO; DERWENT	OR	OFF	2005/06/21 06:56

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L1	42	bunton-william\$.in.	US-PGPUB; USPAT; EPO; DERWENT	OR	OFF	2005/06/21 06:42
L2	83	krause-john\$.in.	US-PGPUB; USPAT; EPO; DERWENT	OR	OFF	2005/06/21 06:42
L3	8	whiteside-patricia\$.in.	US-PGPUB; USPAT; EPO; DERWENT	OR	OFF	2005/06/21 06:43